



Press Release

Lorentz Solution Collaborates with Cadence to Enhance RFIC Design Solutions

SANTA CLARA, Calif. — June 1, 2011 — Lorentz Solution, Inc., the industry-leading provider of Electromagnetic (EM) design solutions for RF and high speed integrated circuits, today announced a joint marketing agreement (JMA) with Cadence Design Systems, Inc. (NASDAQ: CDNS), a leader in global electronic design innovation, aimed at providing more tightly integrated solutions for RFIC and high-speed analog designers.

Mutual customers have had silicon success using Cadence Virtuoso® Custom IC Design and the Lorentz PeakView® EM Platform together but are facing upcoming challenges as designs and processes technologies become more advanced. The agreement provides both companies the opportunity to address these emerging challenges by working together more closely, developing a flow that delivers greater productivity and predictability to RF design teams.

“Our vision has always been to put easy-to-use full-wave 3D EM design tools directly in hands of designers,” said Jinsong Zhao, President and Founder of Lorentz Solution. “Initially we started this through the Cadence® Connections® program resulting in a combined PeakView and Virtuoso Custom IC Design solution that made EM simulation accessible to designers in ways that stand-alone EM solvers cannot by delivering productivity. This agreement is the next step in fulfilling our vision.”

EM solvers have historically been the domain of experts and not directly integrated in the design flow. Alternatively, design tools in the flow are based on approximation methods such as quasi-static and layout parasitic extraction.

Lorentz provides a full-wave 3D EM Design Platform working directly in the Cadence Virtuoso Analog Design Environment, including the Virtuoso Accelerated Parallel RF Simulator. A complete EM design platform not only provides an integrated EM solver but also adds the critical components of EM simulation modeling, parameterized passive device synthesis and advanced process support like CMP, in a tightly integrated design platform.

The focus of this agreement is tighter flow integration, accurate modeling, and high-capacity co-simulation between circuit and EM simulators and to result in higher silicon realization predictability, better design productivity and improved performance of high-speed and RF circuits.

“Cadence and Lorentz successfully work with the top foundries and leading semiconductor companies, delivering advanced RFIC design for communications, wireless and handset applications,” said David

Desharnais, group director, product marketing, Silicon Realization at Cadence. “One of the foundational pieces of the EDA360 vision is the need for greater cooperation among technology companies to tackle the growing challenges and complexities of chip, SoC and system design. With this new agreement, we are extending our collaboration with Lorentz for the benefit of our mutual customers.”

About Lorentz Solution, Inc

Lorentz Solution, Inc is the industry leader in supplying electromagnetic (EM) design capabilities to RF, high-speed analog and high-speed digital design community. PeakView™ EM Design Platform, Lorentz’s flagship product, is widely adopted by top IDM, fabless companies and semiconductor foundries. Based in Santa Clara, California, USA with initial funding from US-based VC firms, Lorentz Solution is continuing its multi-year profitable growth.

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